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PROGRAMMABLE WORD WIDTH AND  
PROGRAMMABLE PRIORITY**(76) Inventors: **Bindiganavale S. Nataraj**, Cupertino,  
CA (US); **Nilesh A. Gharla**, San Jose,  
CA (US); **Rupesh R. Roy**, Santa Clara,  
CA (US); **Jose P. Pereira**, Cupertino,  
CA (US); **Varadarajan Srinivasan**, Los  
Altos Hills, CA (US); **Sandeep  
Khanna**, Santa Clara, CA (US); **Hok F.  
Wong**, Cupertino, CA (US)Correspondence Address:  
**Charles E. Shemwell**  
Suite 204  
998 East El Camino Real  
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(52) U.S. Cl. .... **711/108; 711/158**(21) Appl. No.: **10/000,122**(22) Filed: **Oct. 31, 2001****Related U.S. Application Data**(63) Continuation-in-part of application No. 09/406,170,  
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09/940,832, filed on Aug. 27, 2001.(57) **ABSTRACT**

A content addressable memory (CAM) device including a CAM array and a priority index table. The CAM array has a plurality of rows of CAM cells, each row including a plurality of row segments and being adapted to store a data word that spans a selectable number of the row segments. The priority index table is coupled to the plurality of rows of CAM cells and is adapted to store a plurality of priority numbers, each priority number being indicative of a priority of a corresponding data word stored in the CAM array.

